Indeeksha Semiconductor Training Program

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Learn | Grow | Prosper

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About Us

- Indeeksha Semiconductor Training program is the expansion of renown Indicus Technology by partnering with incredible industrial team to accelerate the mission of the institute to reach out to trainees with large potential and cater the semiconductor trainings with latest cutting-edge technologies.
- The team comprising mentors with close to decade of experience in training and industry partners having industry experience of 15 years in semiconductor area.
- We offer various aspects of semiconductor training require for ASIC/FPGA chip design projects.
- The unique approach we take in imparting training that more focus on in-depth concepts rather than superficial course differentiates us from other training institutes and same is resonated by the success of our alumni.

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Our Mission

- Our mission is to continuously contribute to society by providing world class training and skill development to our trainees and partner companies, and became an eminent institution through knowledge, research and commitment.

Our Vision

- To be recognized as one of the most admired companies in our industry because of our commitments to clients, employees and business partners.

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Training Offered

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ASIC/FPGA Verification

ASIC/FPGA Design

Key Trainings

Physical Design (Upcoming)

Analog Design and Layout (Upcoming)

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Training Offered – ASIC/FPGA Design Verification

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ASIC/FPGA Design Verification training covers following aspects at high level:

- Digital Design
- Verification concepts and test planning
- Verilog HDL
- System Verilog
- Universal Verification Methodology (UVM)
- Test bench architecture concepts
- Industry Standard Protocol concepts
- Scripting: Python
- Platform: Linux and gvim

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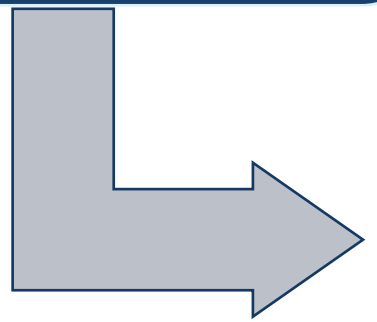
Training Process – Highlights

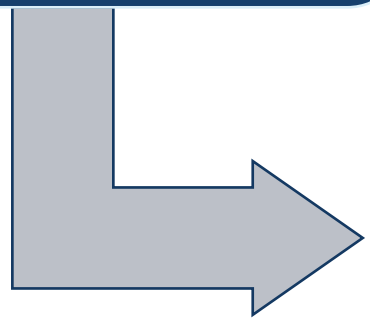
Complete Understanding of the basics by giving regular session on various concepts of each language

- Digital, Verilog, SV, UVM, Scripting etc.
- A well-planned topics by Industry experts



A well-developed labs and assignments to gets complete hands on the topic.

- Topics labs, Tricky Assignment, Real time problems and many more



Review of the work done at regular interval

- A complete review for correctness, approach, and Industry expectation for each individual trainee.

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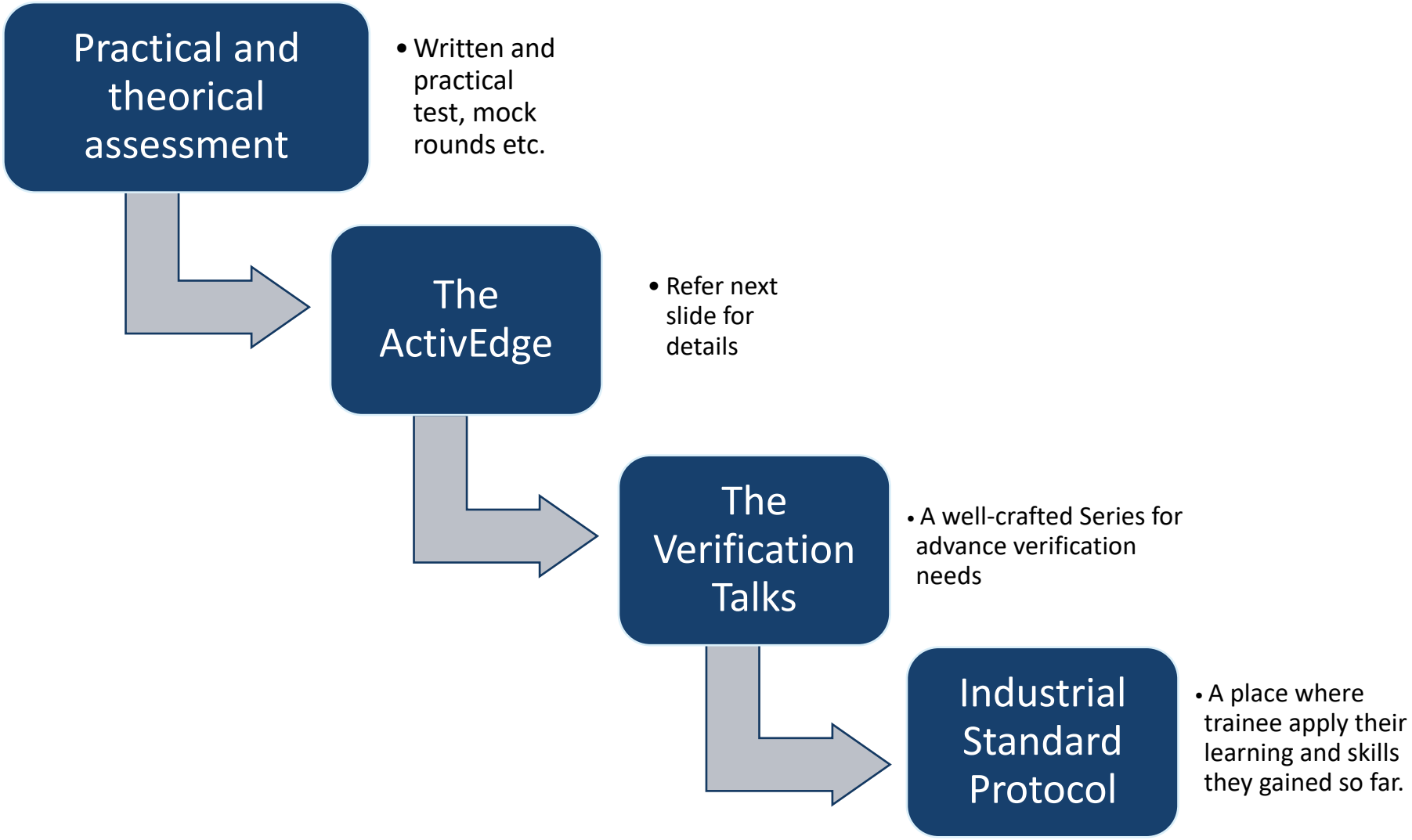
Training Process – Highlights



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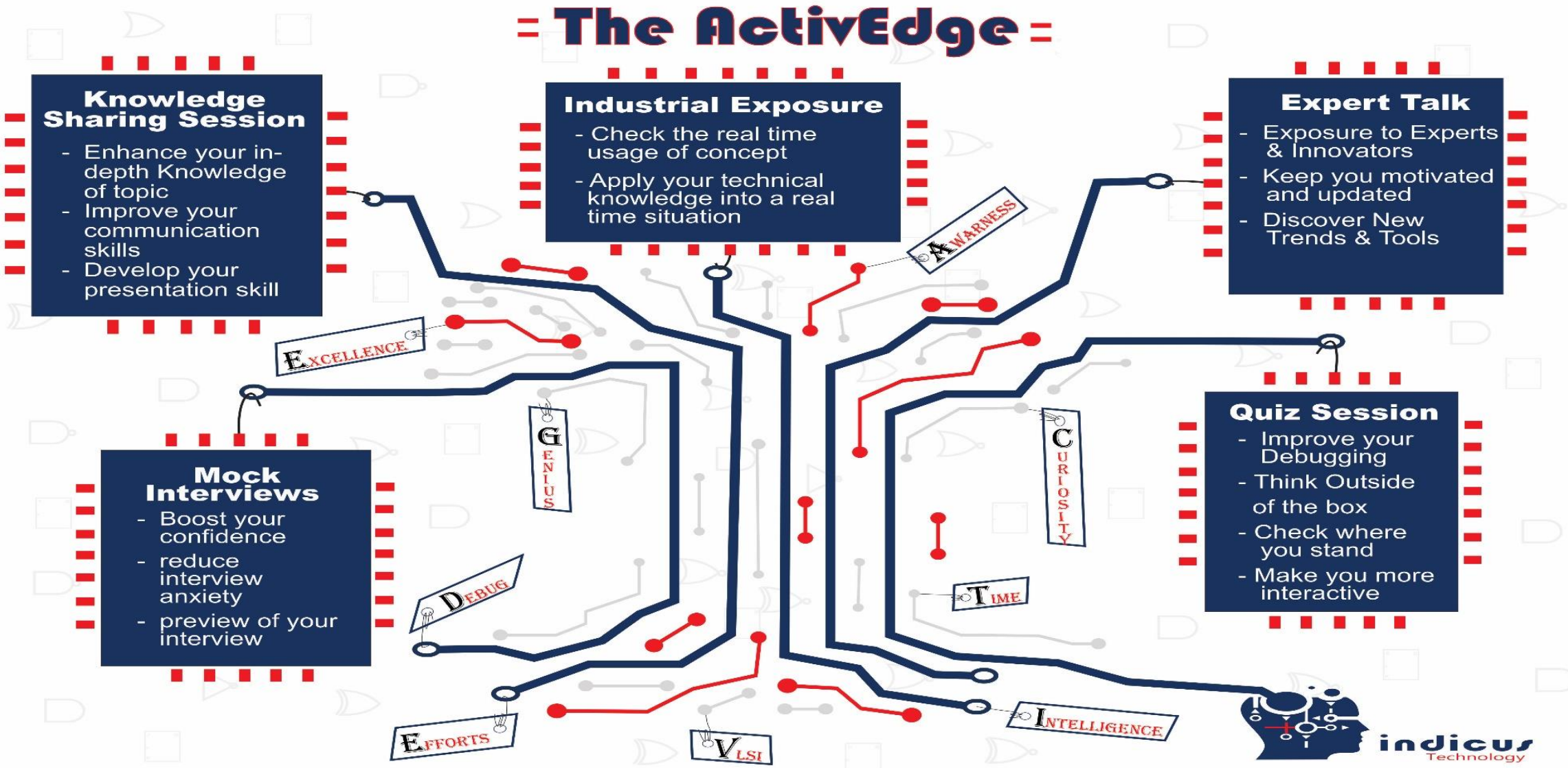
Training Process – Highlights



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Verification Talk:

As we know, the fundamentals/basics are not only sufficient/adequate, but we also need a strong/robust discussion to present their real/actual use cases. To learn the actual verification, a deep dive into the concepts is needed. The verification Talk is a well-crafted series to bring you to the advance verification level. It takes you from “art of specification reading” to “the verification closure”.

- The Verification Plan
- Testcase strategy
- What is VIP?
- How to write a Transaction class?
- How to write a verification component ?
- The Debugging

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Our Happy Customers

Many more continue...

SCALEDGE

AumRaj

Blueberry Semiconductors Pvt. Ltd.

Softnautics

Alpha-Numero

L&T Technology Services

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Our Team – Primary Mentor

Vishnu Vaishnav

- He brings over a decade of experience in the semiconductor industry as a design and verification professional. He is an experienced mentor with a demonstrated history of working in the semiconductor (VLSI) industry. Throughout his career, he has developed skills and abilities to simplify complex and challenging methodologies. Strong technical awareness and skills with a strong sense of opportunity and ability to apply and execute. He has the knowledge and expertise, required to successfully carry out the project, which creates a great influence on his ability.

- For more than 8 years, he has been mentoring engineers in the VLSI front-end domain and grooming them with advanced VLSI-DV skills. He has trained over 2,000 engineers to date. His passion for leadership and belief in contributing to society make him the ideal mentor to develop the skills of the next generation and transform them into efficient and successful resources.

- He has expertise in coverage-driven verification, constraint-random verification, assertion-based verification, EDA, RTL coding, Field-Programmable Gate Arrays (FPGA), Verilog HDL, System Verilog, Python script, Digital and universal verification methodology (UVM). Strong professional with an engineer's degree focused on electronics and communications engineering.

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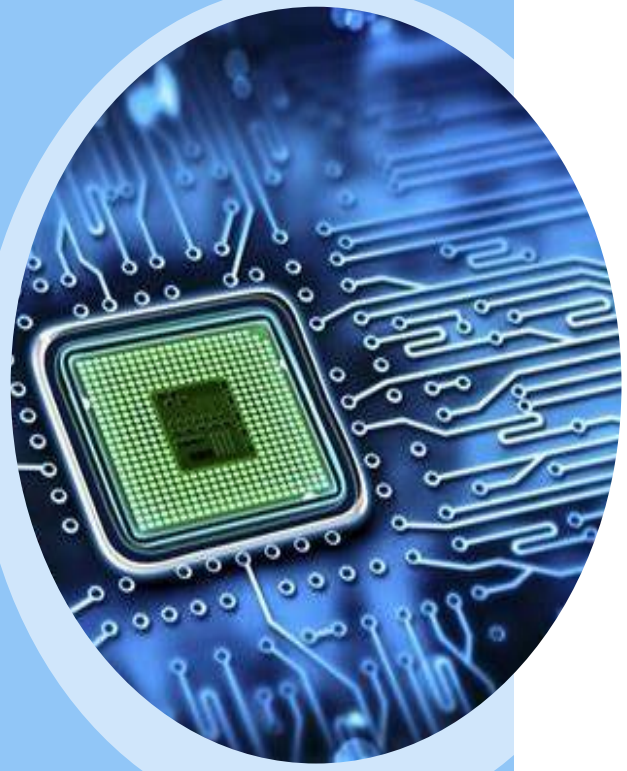
Our Team – Industrial Partner verification experience



IP Verification, ASIC/SOC Verification, FPGA Verification, Reusable VIP Development, HW Testing and Validation, AMS, RAL model Integration, Compute Die Verification, Gate-Level Simulation, DSP Verification, D0-254 Compliance verification

Protocols/Technology: PCIe, CXL 2.0, USB, Ethernet, MIPI CSI/DSI, DPI, AMBA protocols, ARM Processor, CPU Architecture, CAN and IPC Multicore Controller, ARM micro-Controller, Audio/Video Standard, ARINC 429/818, HDMI, DisplayPort

Memory Standards: DDR, LPDDR, Flash memories – NAND/NOR, eMMC, SRAM, NVM

Language/Methodology: System Verilog, OVM, UVM, VHDL, Verilog, C-based Testing for SOC, C, C++

Tools Expertise: Synopsys VCS, Verdi, Cadence xcelium, IUS, Siemens Questasim, Visualizer, VIVADO

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Industry Testimonial

"I have known Vishnu for years and observed being evolved into full fledged mentor with touch of industry. We have hired many trained candidates who got training from him; many of the hired engineers have become key members to the team. It is very apparent for anyone to notice steadfast dedication of Vishnu towards training but more than that it is really appreciating to know his higher motive of spreading semiconductor skills as much as possible to the seekers. Our thoughts match where he really wants trainee to learn approach and concepts more than platform, because platform may change time to time but if one learn concepts and approach, he/she can adapt to any platforms"

Miheer Bariya
Technical Manager,
Alpha Numero Pvt. Ltd.

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"To be working with Team Indicus we have observed much of the positive outcomes as the trained resources/candidates we are getting from them are the best as they easily adapt to the new things and are much productive as they quickly start handling/working with our clients as well. Team Indicus is always supportive in terms of sharing/passing the knowledge to the resources whenever needed. At times it is also observed by our team whenever we have urgent requirement for resources, team indicus has provided the same on urgent basis. Overall if one can choose team indicus for there professional training then they will be having the promising future towards the semiconductor domain.“

Rushang Shah
Associate Director
Eximus

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“The SV/UVM training conducted by Vishnu from indicus technology was very useful and best learning experience to our team.
The classes were very interactive, and topics provided for practicing exercises are very good and helpful. Overall very much happy and recommend others to join the institute.”

Sharanabasappa MB
Delivery Manager
LT Technology Services Ltd

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“I know Vishnu professionally & personally very well. Worked with him for Thales Aerospace project. He is very talented & knowledgeable person. He initially handled complex part of the project & later took over full FPGA Verification responsibility. He guided juniors very well to bring them upto speed so that they could start contributing to the project. He is very patient with subordinates & explains them the difficult portion of the code in simple terms. He is very hard working & smart individual contributor as well as technical lead. I would recommend him for any complex work & also for any training needs.”

Kapil Patel
Engineering Manager,
Juniper Networks

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Industry Testimonial

- Indeeksha is a great platform for students aspiring to work in VLSI field. The Verification course at Indeeksha is very well structured which will help students to crack the interview. Project at the end of every major module is adding a good advantage for students to understand the concept deeply.

- It been 10 years since I know vishnu, He is very Honest and talented guy. Super dedicated to his work. Best thing about him Is to turning the complex things into simplest form so that anybody can understand. I highly recommended Indeekha to all fresh graduates and even industry folks to upskill their knowledge.

Mr. Khushmit Pandya
Lead Verification Engineer,
arm

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Industry Testimonial

- I know Vishnu professionally and personally we stayed almost a 3 year together. He has incredible courage and patience; he has the knowledge of managing things he is very passionate about his work he is very talented and intelligent guy. I had worked with him for a long period of time . He is very smart and hard-working person. He is a nice person to interact too.

- He always discuss about ideas and their implementation. I went through the VLSI brochure that he has modeled to educate students on VLSI field. it looks excellent to me covering all the aspects right from RTL Coding, Testbench modeling, Linux, and scripting. A complete package by knowing which one can easily enter into VLSI industry. I highly recommend Indicus to the students aspiring to work in VLSI field.

Mr. Krunal Kansara
Lead Design Engineer,
Cadence Design Systems India Pvt Ltd

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Companies where Our Trainees Placed

Truechip
The Verification IP Specialist

WIPRO
Applying Thought

GLOBALTECH™ INDIA
Expanding Globally by Intelligent Solutions

SyBridge
TECHNOLOGIES
Bridging Design, Manufacturing, & Technology

Provino Technologies

SiFive

SYNAPSE design

QUEST
BORN TO ENGINEER

CENTRA TECHNOLOGY, INC.

VERIFAST

AumRaj

eInfochips

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Companies where Our Trainees Placed

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Alpha-Numerc

Softnautics

smartsoc solutions

Blueberry Semiconductors Pvt. Ltd.

SiSoC

AVERICK SEMICONDUCTOR

SANKALP SEMICONDUCTOR
An HCLTech Company

dxcorr

EXIMIUS
Excellence Everyday

NUMASCALE®

ARASU
Impassioned Engineering

ATRIA

JUNTRAN

Green IP Core
PlusQO Corp. Pvt. Ltd.

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